



"Techno - Social Excellence"  
**Marathwada Mitramandal's**  
**Institute of Technology (MMIT)**

Accredited with "A" Grade by NAAC

Survey No. 35, Vadgaon Shinde Road, Lohgaon, Pune - 411 047

Approved by AICTE, New Delhi, Recognised by DTE, M.S.Mumbai, Affiliated to Savitribai Phule Pune University

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DTE Institute Code : 6203



## Unit Test II (Online)

Academic Year: 2020-21

Semester: II

Date: 25 .03.2021

Class : SE Computer

Course Code: 210255

Max. Marks: 30

Div: A&B

Course Name: Microprocessor

Time: 60 Minutes

### Question Paper Mapping of Bloom's Taxonomy level and CO

Q. No	Question	Marks	Blooms level	CO
1	<b>The Segmentation in 80386 facilitates -----</b> A. Protection Memory management with B. Multitasking C. Isolation and memory management <b>D. All of these</b>	2	L1	CO1
2	What does the INTn instruction push onto the stack the CALL A. Segment Address <b>B. Flags</b> C. Offset Address D. None of above	2	L2	CO2
3	IF CX =1234H and BX=75FDH what is the value stored in CX after the execution of the following instruction: TEST CX, BX <b>A.1234H</b> B. 77FDH C. 75FDH D. 1032H	2	L3	CO2
4	The errors that can be pointed out by the assembler are <b>A. Syntax error</b> B. Semantic error C. Logical error D. None of the above	2	L1	CO1
5	Pseudo instructions are <b>A. Machine instructions</b> B. Logical instruction C. Micro instruction D. instructions to assemble	2	L1	CO1



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6	The negative numbers in the binary system can be represented by A. Sign magnitude B. 1's complement <b>C. 2's complement</b> D. All of the above	2	L1	CO1
7	What is the output of the following code? PUSH AL A. Decrement SP by 2 & push a word to stack B. Increment SP by 2 & push a word to stack C. Decrement SP by 2 & push a AL to stack <b>D. Illegal</b>	2	L5	CO1
8	The ret instruction modifies the <b>A. IP or CS: IP and SP register</b> B. flags register C. BP register D. none of the previous	2	L2	CO1
9	The call instruction modifies A. the flags register <b>B. stack pointer</b> C. BP register D. none of the these	2	L2	CO2
10	The call instruction modifies A. the flags register <b>B. IP or CS: IP IP or CS: IP</b> C. BP register D. none of the these	2	L2	CO2
11	Conditional instructions typically inspect the A. IP or CS: IP B. destination register <b>C. flag register</b> D. accumulator	2	L1	CO2
12	The CMP instruction modifies the constant reference A. IP or CS: IP B. destination register <b>C. flag register</b> D. segment register	2	L2	CO2



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13	The call instruction stores the return address for a subprogram A. on the stack B. in the memory address register C. in the CS:IP D. does not involve using the return address	2	L2	CO2
14	The output of the linker (LD command) is stored in a file with the extension A. .lis B. .o C. .obj D. .lnk	2	L1	CO2
15	An interrupt instruction _____. A. causes an unconditional transfer of control Only functions B. causes a conditional transfer of control C. modifies the status register D. is an I/O instruction	2	L2	CO2

**Subject Teacher**

**Prof. Pranjali V. Deshmukh**